

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S65	821	714/30.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 13:10
S66	458	714/45.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 13:10
S67	623	712/227.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 13:09
S68	388	717/128.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 13:08
S69	285	717/128.ccls. and (trace trace traced tracing traces)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:31
S70	202	712/227.ccls. and (trace trace traced tracing traces)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:28
S71	245	714/45.ccls. and (trace trace traced tracing traces)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:26
S72	184	714/30.ccls. and (trace trace traced tracing traces)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:30
S73	1	10/729326	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/23 13:26

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S74	28	714/45.ccls. and (trace trace traced tracing traces) and @pd>"20060613"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:26
S75	11	712/227.ccls. and (trace trace traced tracing traces) and @pd>"20060620"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:28
S76	24	714/30.ccls. and (trace trace traced tracing traces) and @pd>"20060620"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:30
S77	30	717/128.ccls. and (trace trace traced tracing traces) and @pd>"20060620"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:34

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S80	1	(new adj secondary adj code adj execution adj start adj point adj signal).clm. and (timing adj trace adj stream).clm. and (program adj counter adj trace adj apparatus).clm. and (synchronization adj apparatus).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:47
S81	1	(new adj secondary adj code adj execution adj start adj point adj signal).clm. and (timing adj trace adj stream).clm. and (program adj counter adj trace adj apparatus).clm. and (data adj trace adj stream).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:53
S82	1	(new adj secondary adj code adj execution adj start adj point adj signal).clm. and ( trace adj signal adj stream).clm. and (target adj processor adj clock).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:54
S83	1	(new adj secondary adj code adj execution adj start adj point adj signal).clm. and (program adj counter adj trace).clm. and (sync adj marker adj assembly adj apparatus).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 11:55



"interrupt service routine" "program counter" Search

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[J Tsai](#)  
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[K Fang](#)  
[Y Bi](#)  
[C Shin](#)

JM Cheng - newit.gsu.unibel.by  
 ... as stack pointer, link register, and **program counter**. ... Figure 11 Instruction execution  
**trace** of ARM ... Argument BL 0x05b4 //Branch into **Interrupt Service routine** ...  
[Related Articles](#) - [View as HTML](#) - [Web Search](#)

[Efficient Interrupt Processing on the M-CORE Architecture](#)  
 J Vaglica - bibpc02.us.es  
 ... and disable **trace** mode and further exceptions ... demonstrate a basic **Interrupt service**  
**routine**, software prioritization ... restores the **program counter** and processor ...  
[View as HTML](#) - [Web Search](#)

[Device to monitor press fittings - group of 3 »](#)  
 ER Erwin - US Patent 4,692,775, 1987 - Google Patents  
 ... by that portion of the **trace** correspond -ing to ... interrupt instruction returns the  
**program counter** of the CPU ... returning from the **Interrupt service routine** at step ...  
[Related Articles](#) - [Web Search](#)

[Method and apparatus for synchronizing data in a host memory with data in target MCU memory - group of 2 »](#)  
 D Mulchandani, R Gray - US Patent 5,680,542, 1997 - Google Patents  
 ... provided so a user can run his application, **trace** ... a user externally while debugging  
 to **return** his applica- ... 60 "reset" loads the MCU **program counter** ("pc") with ...  
[Cited by 18](#) - [Related Articles](#) - [Web Search](#)

[AN ARCHITECTURE FOR THE DIRECT EXECUTION OF THE FORTH PROGRAMMING LANGUAGE](#)  
 JR Hayes, ME Fraeman, RL Williams, T Zaremba - portal.acm.org  
 ... If the FL is set, the **program counter** has the ... location must be available for use  
 by the **Interrupt service routine**, ... A **trace** of the stack depths from the first ...  
[Cited by 13](#) - [Related Articles](#) - [Web Search](#)

[PicoBlaze 8-Bit Microcontroller for Virtex-E and Spartan-II/IIE Devices - group of 26 »](#)  
 K Chapman - Xilinx Application Notes, February, 2003 - programmable.com  
 ... **TRACE** Report ... It concludes an **Interrupt service routine**. The RETURNI is unconditional  
 and always loads the **program counter** (PC) with the last address on the ...  
[Cited by 5](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

[A replay mechanism for non-interference real-time software testing and debugging](#)  
 JJP Tsai, KY Fang, HY Chen - Software Maintenance, 1989, Proceedings., Conference on, 1989 -  
 ieeexplore.ieee.org  
 ... The processor status, registers, and **program counter** may then ... interrupt signal triggers  
 an **Interrupt service routine** in the ... eg step-by-step instruction **trace**). ...  
[Cited by 1](#) - [Related Articles](#) - [Web Search](#)

[Asynchronous multiple buffered communications interface having an independent microprocessor for ... - group of 3 »](#)  
 LJ Tierney - US Patent 4,682,304, 1987 - Google Patents  
 Page 1. United States Patent Tierney [ii] Patent Number: [45] Date of Patent:  
 [54] [75] [73] [21] [22] [63] [51] [52] [58] [56] ASYNCHRONOUS ...  
[Cited by 22](#) - [Related Articles](#) - [Web Search](#)

[Trace fifo management - group of 2 »](#)  
 M Buser, G Laurenti, GM Nandyal - US Patent 6,507,921, 2003 - Google Patents  
 ... (54) **TRACE FIFO MANAGEMENT** ... A **trace** FIFO (800) is provided for tracing a sequence  
 of instruction addresses to assist with software or hardware debugging. ...  
[Cited by 4](#) - [Related Articles](#) - [Web Search](#)

[A Roving Monitoring Processor for Detection of Control Flow Errors in Multiple Processor Systems "](#)  
 SP Tomas, JP Shen - IEEE International Conference on Computer Design - 128.2.129.29

... monitoring processor that uses them to trace a signature ... NI: INSTRUCTION 3 .....  
**RETURN** ... creates a separate signature graph for the **Interrupt service routine**. ...  
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1125	((interrupt interrupts) with (trace traces tracing traced))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 13:58
L2	308	((interrupt interrupts) with (trace traces tracing traced)) and (program adj counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 15:18
L3	101	((interrupt interrupts) with (trace traces tracing traced)) same (program adj counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/20 13:58